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TX6251C

1A Low Power LDO

Features

- Low voltage drop: 0.06V@100mA
- High input voltage: 8.0V
- Low temperature coefficient
- Low Quiescent Current: 8uA at 5.0V
- Output voltage accuracy: tolerance $\pm 2\%$

Applications

- Battery-powered equipment
- Hand-Hold Equipment
- GRS Receivers
- Wireless LAN

General Description

The TX6251C series is a group of positive voltage output, three-pin regulators that provide a high current even when the input/output voltage differential is small. Low power consumption and high accuracy is achieved through CMOS and laser trimming technologies.

and a current limited output driver. Transient response to a load variations have improved in comparison to the existing series.

SOT89-3, SOT23-3 packages are available.

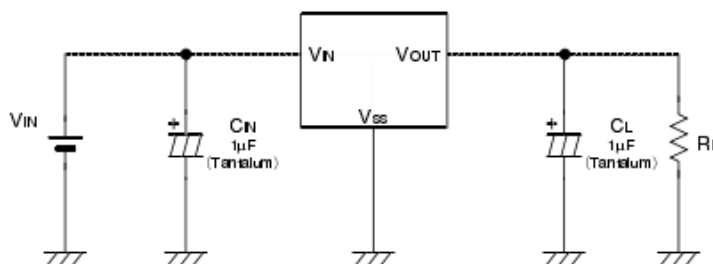
The TX6251C consists of a high-precision

Order Information

TX6251C①②③④

Designator	Symbol	Description
①	P	Package: SOT89A
	PB	Package: SOT89B
	M	Package: SOT23-3
②③	Integer	Output Voltage(1.2~5.0V)
④	R	RoHS / Pb Free
	G	Halogen Free

Typical Application

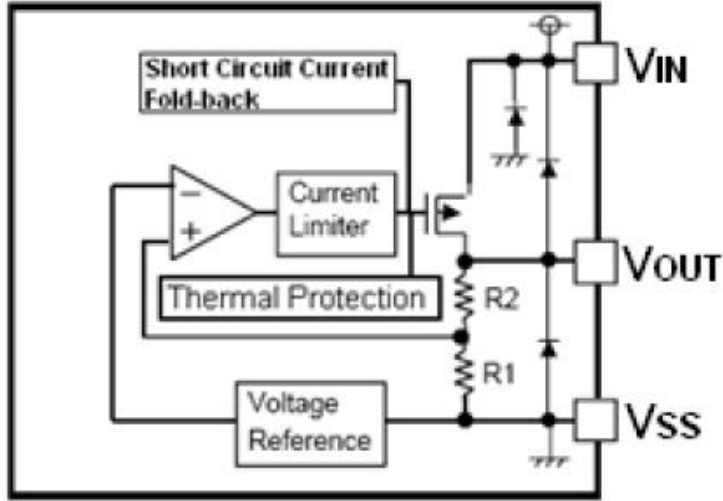


Note1: Input capacitor $C_{IN}=1\mu F$.

Note2: Output capacitor $C_{OUT}=1\mu F/6.8\mu F$ (1uF Tantalum capacitor or 6.8uF ceramic capacitor is recommended).



Block Diagram





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Pin Assignment

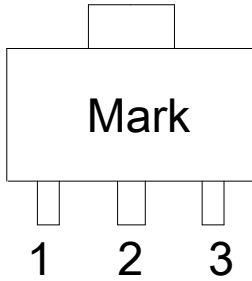


Table1 TX6251C series (SOT89A PKG)

PIN NO.	PIN NAME	FUNCTION
1	GND	GND pin
2	VIN	Input voltage pin
3	VOUT	Output voltage pin

Table2 TX6251C series (SOT89B PKG)

PIN NO.	PIN NAME	FUNCTION
1	VIN	Input voltage pin
2	GND	GND pin
3	VOUT	Output voltage pin

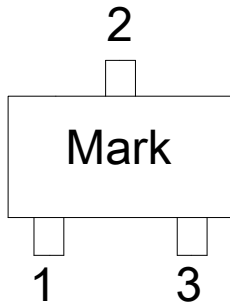


Table3 TX6251C series (SOT23-3 PKG)

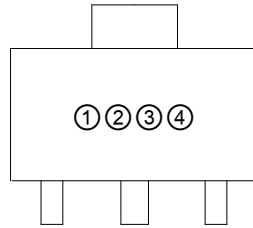
PIN NO.	PIN NAME	FUNCTION
1	GND	GND pin
2	VIN	Input voltage pin
3	VOUT	Output voltage pin



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Marking Rule SOT89-3



① represents the integer of the output voltage

SYMBOL	VOLTAGE(V)
1	1.②
2	2.②
3	3.②
4	4.②
5	5.②
6	6.②

② represents the decimal number of the output voltage

SYMBOL	VOLTAGE(V)	SYMBOL	VOLTAGE(V)
A	①. 0	F	①. 5
B	①. 1	H	①. 6
C	①. 2	K	①. 7
D	①. 3	L	①. 8
E	①. 4	M	①. 9

③ based on internal standards

SYMBOL
0

④ represents the assembly LOT No.
Based on internal standards



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Absolute Maximum Ratings

Supply Voltage-0.3V to 8.5V Operating Temperature-40°C to 85°C
 Output Current.....1.1A Storage Temperature-40°C to 125°C

Thermal Information

Symbol	Parameter	Package	Max.	Unit
θ_{JA}	Thermal Resistance (Junction to Ambient) (Assume no ambient airflow, no heat sink)	SOT23-3	500	°C/W
		SOT89	200	°C/W
P_D	Power Dissipation	SOT23-3	0.25	W
		SOT89	0.50	W

Note: P_D is measured at $T_a = 25^\circ\text{C}$

Note: These are stress ratings only. Stresses exceeding the range specified under “Absolute Maximum Ratings” may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

Electrical Characteristics

TX6251C for any output voltage

($T_a = 25^\circ\text{C}$)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V_{OUT}	Output Voltage	$V_{in} = V_{out} + 1V$ $1.0\text{mA} \leq I_{out} \leq 30\text{mA}$	$V_{out} \times 0.98$	--	$V_{out} \times 1.02$	V
I_{OUT}	Output Current*1	$V_{in} - V_{out} = 1V$	--	1000	--	mA
V_{DROP}	Low dropout*2	Refer to the next table				
$\frac{\Delta V_{OUT}}{\Delta V_{IN} \times V_{OUT}}$	Line Regulation	$1.6V \leq V_{in} \leq 8V$ $I_{out} = 100\text{mA}$	--	0.05	0.2	%/V
$\Delta V_{OUT} / \Delta I_{OUT}$	$\Delta V_{out} / \Delta I_{out}$	$V_{in} = V_{out} + 1V$ $1.0\text{mA} \leq I_{out} \leq 100\text{mA}$	--	12	30	mV
Output voltage Temperature Coefficiency	$\Delta V_{out} / (T_a \cdot V_{out})$	$I_{out} = 30\text{mA}$ $0^\circ\text{C} \leq T_a \leq 70^\circ\text{C}$	--	± 100	--	Ppm/°C
Supply Current	I_{SS}	--	--	8	12	uA
Input Voltage	V_{in}	--	--	--	8	V



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Electrical Characteristics by Output Voltage:

Output Voltage Vout(V)	Dropout Voltage Vdif (V)		
	Conditions	Typ.	Max.
$V_{out} \leq 2.0V$	I _{out} =60 mA	0.05	0.08
$2.0 < V_{out} \leq 3.0$	I _{out} =80 mA	0.05	0.08
$3.0 < V_{out} \leq 4.0$	I _{out} =100 mA	0.06	0.08
$4.0 < V_{out} \leq 5.0$		0.05	0.08
$3.0 < V_{out} \leq 4.0$	I _{out} =200 mA	0.13	0.16
$4.0 < V_{out} \leq 5.0$		0.12	0.16
$3.0 < V_{out} \leq 4.0$	I _{out} =1000 mA	0.65	0.8
$4.0 < V_{out} \leq 5.0$		0.6	0.8



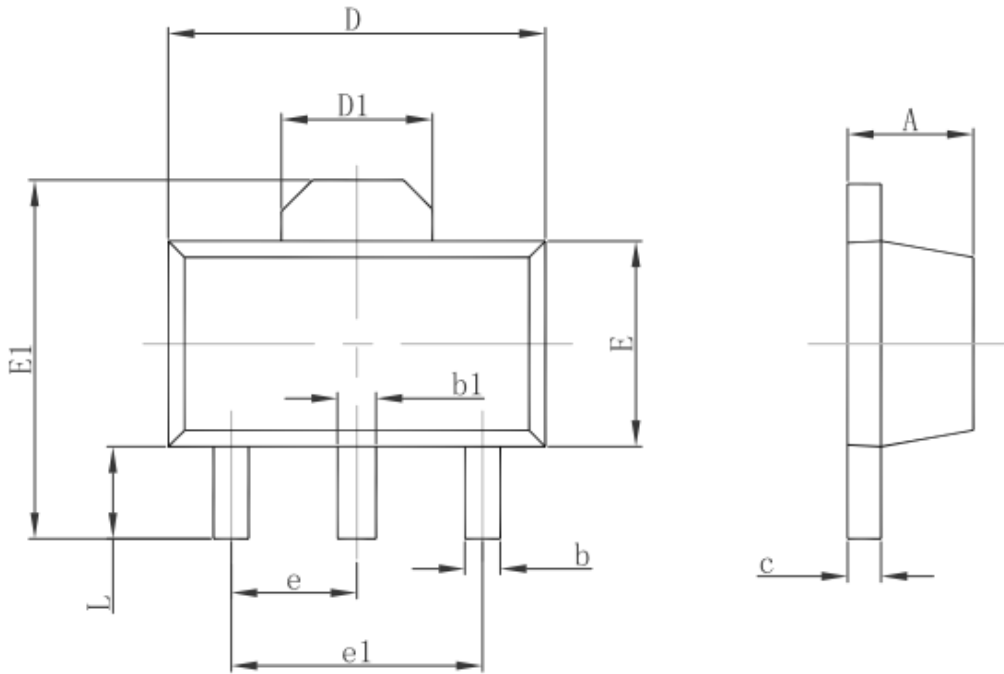
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Package Information

3-pin SOT89 Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.400	1.600	0.055	0.063
b	0.320	0.520	0.013	0.020
b1	0.400	0.580	0.016	0.023
c	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.550 REF.		0.061 REF.	
E	2.300	2.600	0.091	0.102
E1	3.940	4.250	0.155	0.167
e	1.500 TYP.		0.060 TYP.	
e1	3.000 TYP.		0.118 TYP.	
L	0.900	1.200	0.035	0.047

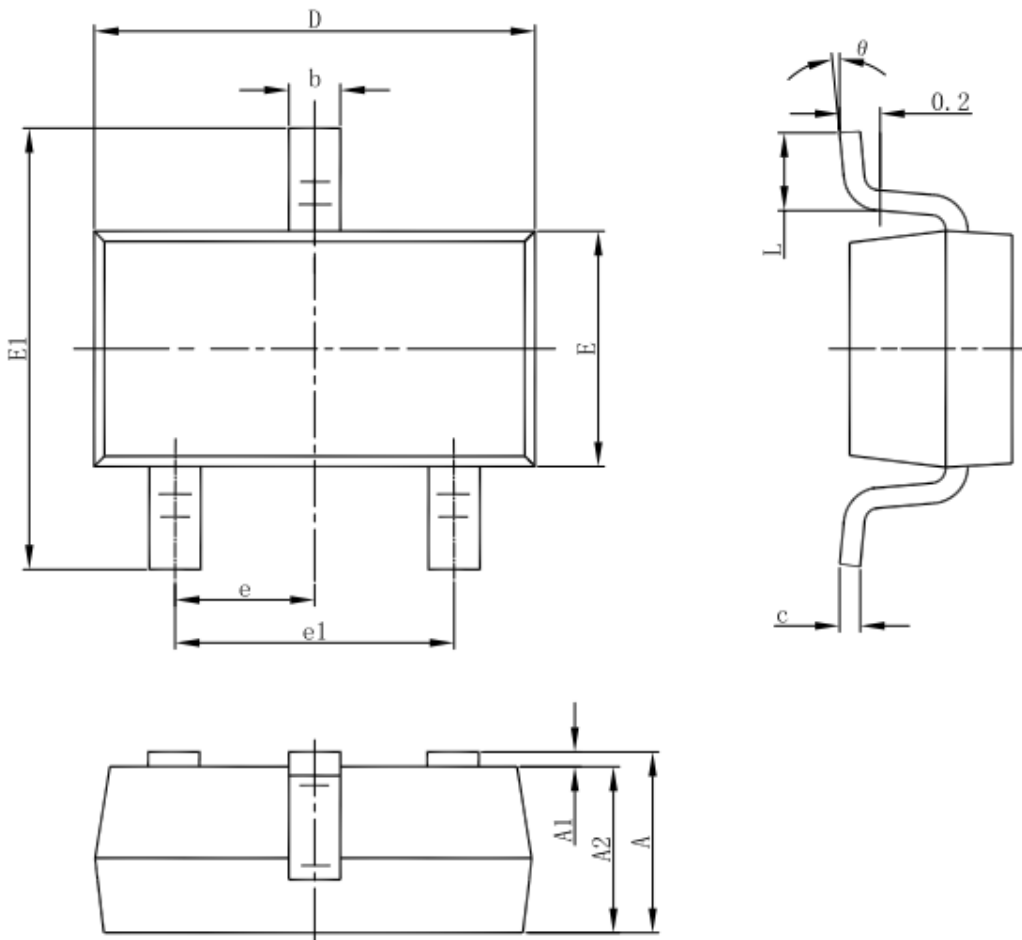


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SOT23-3 Outline Dimensions

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°





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